FIG.1

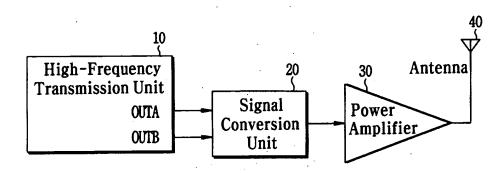


FIG.2 72 73 71 30 70 Bias/Control Block 60 (80 50 First Second Intermediate Amplification Circuit Unit Amplification Circuit Unit OUT/ Impedance Matching Unit IN VCC2

VCC1

FIG.3

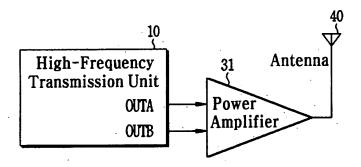


FIG.4

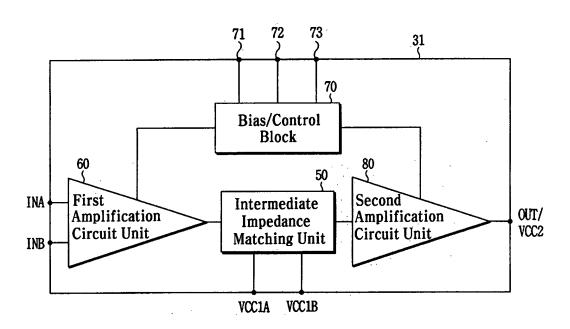


FIG.5

